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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/679,623	10/06/2003	Rene Rollig	5500-92201	3337
53806	7590	09/11/2006		EXAMINER
				VIDWAN, JASJITS
			ART UNIT	PAPER NUMBER
				2182

DATE MAILED: 09/11/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/679,623	ROLLIG ET AL.	
	Examiner	Art Unit	
	Jasjit S. Vidwan	2182	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 16 June 2006.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-31 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-31 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 06 October 2003 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

ftz m.flw

FRITZ FLEMING

SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2100

9/5/2006

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 8/25/2004
- 4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: _____

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group I, Claims 1-31 in the reply filed on 6/16/2006 is acknowledged. Claims 1-31 are pending. Claims 32-95 have been withdrawn from consideration.

Drawings

1. Figures 1-23 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the examiner does not accept the changes, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

2. The disclosure is objected to because it contains an embedded hyperlink and/or other form of browser-executable code on page 7, paragraph 2 of the Applicant submitted specification. Applicant is required to delete the embedded hyperlink and/or other form of browser-executable code. See MPEP § 608.01.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) The invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Final
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1-31

4. Claims 1, 10 and 19 are rejected under 35 U.S.C. 102(b) as being anticipated by Applicant

Admitted Prior Art, Figures 1-23 & "Description of the prior art" specification [herein after AAPA].

5. As per Claims 1, 10 and 19, AAPA teaches a SMBus message handler comprising:

- (a) Memory [Fig. 23, element 202] configured to store microcode comprising at least two programs each for handling a bus command protocol and comprising at least one instruction [Fig. 23, element 210, 211, 212].
- (b) Interface to a register configured to identify a starting address of a program in said memory [Fig. 23, SMB_PRTCL]
- (c) Instruction fetch unit [Fig. 23, element 203] configured to read an instruction at an address in said memory [Fig. 23, element 202], said address being specified by a program counter [Fig. 23, "PC"]
- (d) Finite-state machine [201] configured to receive and interpreting the instructions read by said instruction fetch unit [203] and for managing the data transfer between an SMBus [213, 214] interface, and a register set [208] in compliance with said instructions read from said memory.

6. As per Claims 2, 11 and 20, AAPA teaches SMBus message handler wherein said register set complies with the ACPI specification [see Page 6, "Description of prior art", Paragraph 2].

7. As per Claim 3, 12 and 21, AAPA teaches SMBus message handler further comprising an address register array [see Fig. 23, element 207] comprising a plurality of starting addresses of programs stored in said memory, said register comprising an offset for pointing at a specific register in said address register array [see Fig. 23, SMB_PRTCL].

8. As per Claim 4, 13 and 22, AAPA teaches SMBus message handler further comprising a buffer pointer register [Fig. 23, element 206] for pointing at one of a plurality of data registers [Fig. 23, SMB_DATA]; said finite state machine [Fig. 23, element 201] transferring data read from SMBus [Fig. 23, elements 213, 214] interface to the data register at which said buffer pointer register [Fig. 23, element 206] points if said finite-state machine [Fig. 23, element 201] interprets a "receive data to"

instruction; said finite state machine [201] transferring the data read from the data register at which said buffer pointer register points to said SMBus [213, 214] interface if said finite-state machine [201] interprets a “transmits data from” instruction.

9. **As per Claims 5, 14, 23, AAPA teaches SMBus message handler wherein the finite-state machine causes said buffer pointer register [206] to be incremented each time a “transmit data to” or a “transmit data from” instruction is executed [Fig. 23, “Pointer counter”]**

10. **As per Claims 6 and 15, AAPA teaches SMBus message handler further comprising a loop counter [Fig. 23, element 204] for storing the value of a block counter register SMB_BCNT in said loop counter if the finite-state machine executed a “transmit data from SMB_BCNT” instruction; said loop counter being decremented each time a data byte is transmitted to said SMBus [Fig. 23, elements 213, 214] interface while a “transmit data from” instruction is executed and the “transmit data from” instruction be completed when the value of said loop counter reaches zero [204].**

11. **As per Claims 7, 16 and 29, AAPA teaches SMBus message handler further comprising a loop counter [204] and a block counter register [SMB_BCNT] both for storing a byte received from said SMBus [213, 214] interface if the finite-state machine [201] executed a “receive data to SMB_BCNT” instruction, said loop counter [204] being decremented each time a data byte is transmitted to or received from said SMBus [213, 214] interface while a “received data to” instruction is executed and the “received data to” instruction being completed when the value of said loop counter [204] reaches zero.**

12. **As per Claims 8, 17, 30, AAPA teaches SMBus message handler, wherein each instruction comprises one bit indicating as to whether or not an instruction is the last instruction in the program [Fig. 23, element 304].**

13. **As per Claims 9, 18 and 31, AAPA teaches SMBus message handler, wherein each instruction comprises one bit [Fig. 23, element 305] indicating as to whether an instruction is to be executed only once or this instruction is to be executed repeatedly until a loop counter [204] becomes zero, wherein said loop counter is decremented each time an instruction is executed repeatedly.**

14. **As per Claims 24, AAPA teaches a method wherein said transferring step further comprising decrementing a loop counter and checking as to whether said loop counter has a value of zero [Fig. 23, element 204].**
15. **As per Claim 25, AAPA teaches a method wherein said transferring step comprises the sub-steps of interpreting a “transmit data from” instruction and reading the value of a buffer pointer register [206] and transferring the data read from the data register [SMB_DATA] at which the value stored in said buffer pointer register to said SMBus [Fig. 23, element 213, 214] interface**
16. **As per Claim 26, AAPA teaches a method wherein said transferring step further comprises incrementing of said buffer pointer register [Fig. 23, element 206]**
17. **As per Claim 27, AAPA teaches a method wherein the transferring step further comprises decrementing of said loop counter [Fig. 23, element 204]**
18. **As per Claim 28, AAPA teaches a method wherein transferring step comprises:**
 - (a) Interpreting a “transmit data from SMB_BCNT” instruction [213]
 - (b) Storing the value of a block count register SMB_BCNT in a loop counter [204]
 - (c) Transmitting the value of said block count register [SMB_BCNT] to said SMBus interface [212, 214].

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasjit S. Vidwan whose telephone number is (571) 272-7936. The examiner can normally be reached on 8am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, KIM HUYNH can be reached on (571) 272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JSV
9/5/06

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9/5/2006